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This collection of information is required by 37 CFR 1.311. The Information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. Do NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Putents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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Inventor(s):

Amit Singh

Assignee: XILINX, INC.

Serial No.: 10/676,929

conf.no. -7851

Filed:

September 30, 2003

Title: Circuit Clustering During Placement

Docket No.:

X-1495 US

Enclosed: *Return Receipt Postcard

•Fee Transmittal - Part B

Atty/Sec: KAC/pat

Date: August 9, 2006

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